

Fig.1

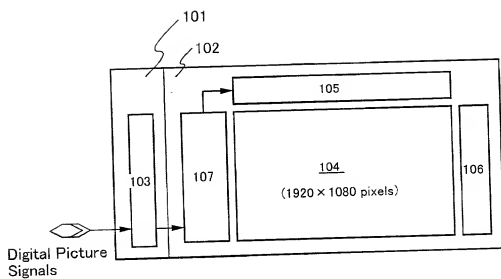


Fig. 2

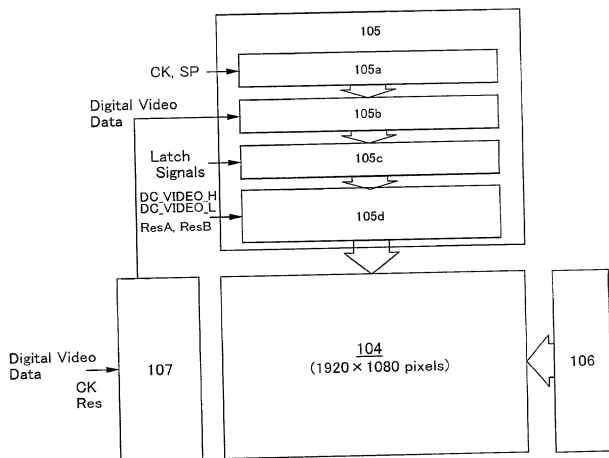


Fig. 3

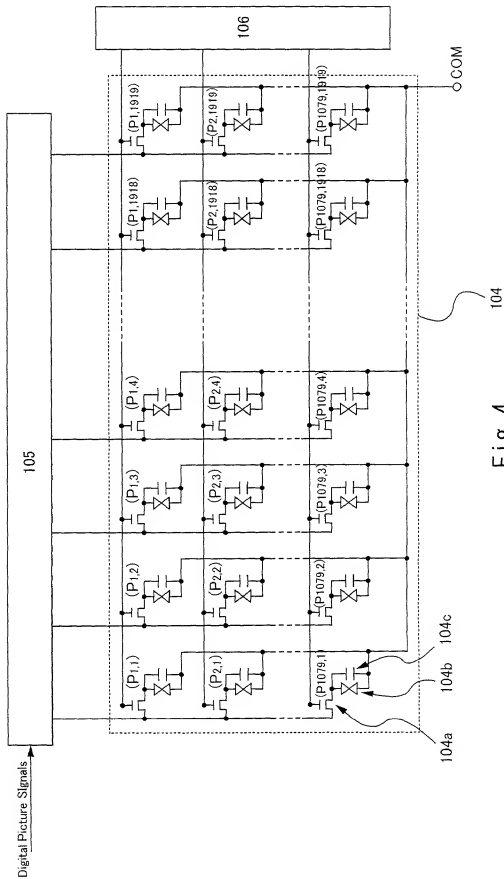


Fig. 4

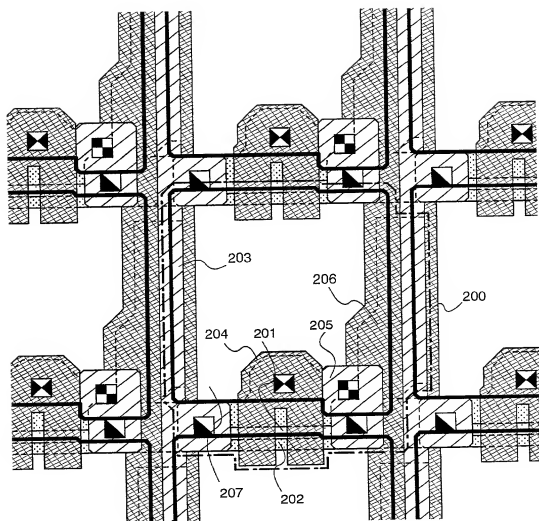


Fig. 5








-  : Semiconductor Layer
-  : Gate Metal (1st & 2nd Conductive Layers)
-  : Wiring
-  : Lower Light Shielding Film
-  : Contact Hole (Lower Light Shielding Film - Gate Metal)
-  : Contact Hole (Semiconductor Layer - Gate Metal - Wiring)
-  : Contact Hole (Wiring - Pixel Electrode)

Fig. 6A

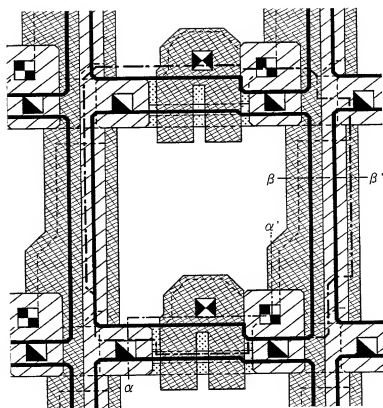
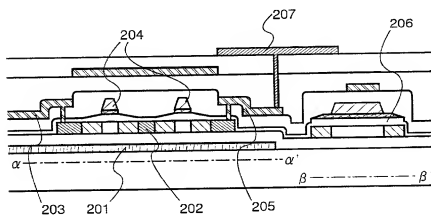


Fig. 6B



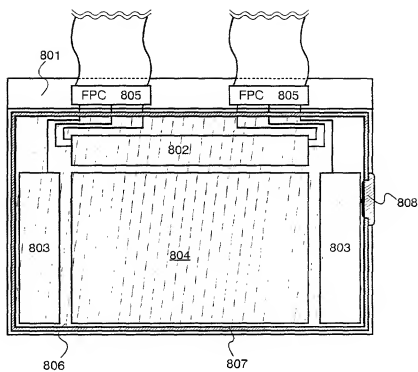


Fig. 7A

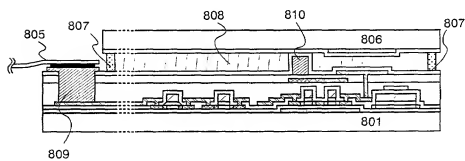


Fig. 7B

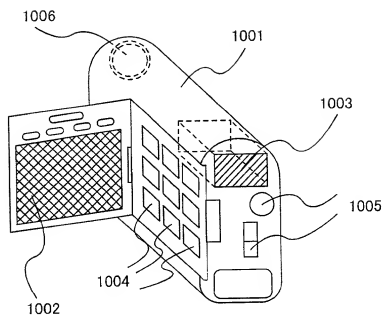


Fig. 8

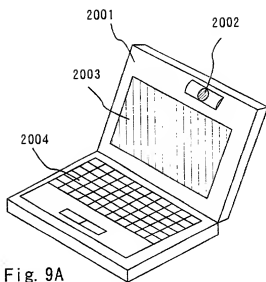


Fig. 9A

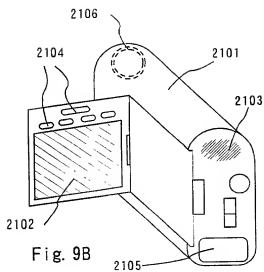


Fig. 9B

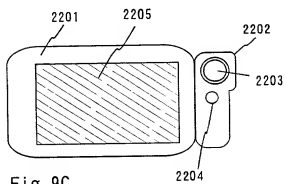


Fig. 9C

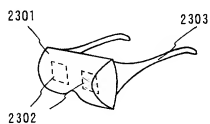


Fig. 9D

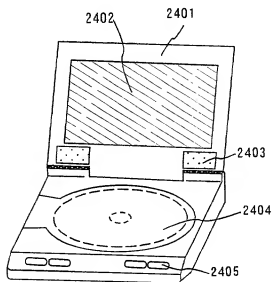


Fig. 9E

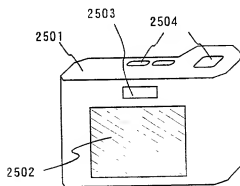


Fig. 9F

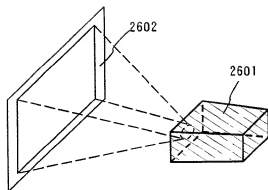


Fig. 10A

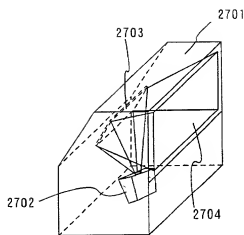


Fig. 10B

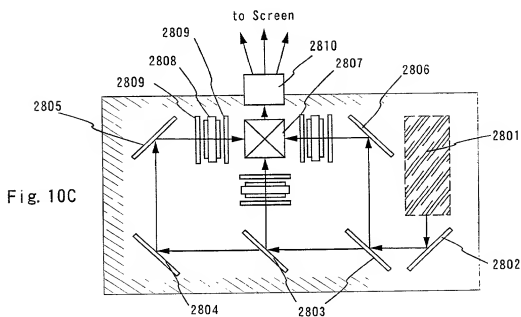


Fig. 10C

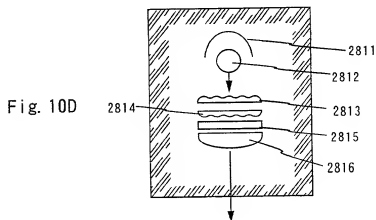


Fig. 10D

Fig. 11A

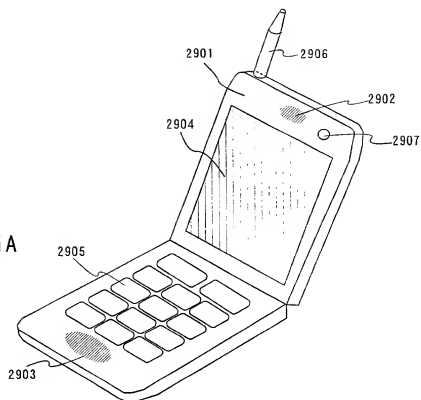


Fig. 11B

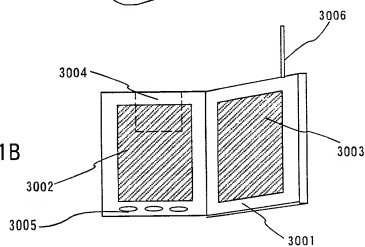
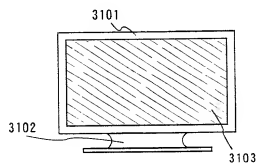


Fig. 11C



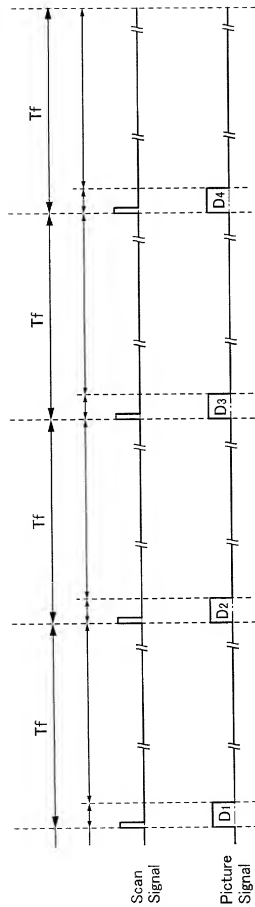


Fig. 12
(Prior Art)